

## **Audio Panel**

making digital broadcasting work Phone +1.902.823.2233 / +33.299.146.332 info@nautel.com / contactdigidia@nautel.com www.nautel.com



## **APPLICATIONS**

- Professional audio patch panel with XLR inputs for analog and digital audio and USB output,
- Multiple digital or analog audio input for all DIGIDIA head end products: the FlexiDAB DAB/DAB+/DMB multiplexer and encoder, the EasyDAB DAB test multiplexer/EDI generator the ALTO DRM content server or the SpanFM Synchronous FM head end system of DIGIDIA multiplexing system,

#### **KEY BENEFITS**

- Field proven, used with DIGIDIA systems in many countries all over the world
- 10 years of sales record
- Adapted for 1+1 redundant systems
- Synchronization between audio panel and head end products
- 100% DIGIDIA in house development
- Easy configuration with user friendly GUIs (locally and remotely)
- Double power supply

## **TECHNICAL SPECIFICATIONS**

#### **INPUTS / OUTPUTS**

Analog Inputs: Female XLR Connectors  $-600\Omega$  balanced

Digital Inputs: Female XLR connectors – 110Ω balanced

Synchronization input: BNC 50Ω connector – 48kHz frequency

Multiplex output: USB 2.0 certified compliant, High speed USB interface (480Mbs)

#### **MECHANICAL**

Dimensions and weight:

 1RU: 483 (19") x 450 x 44 mm; 5 kg, rackable Temperature Range:

Operating: 0°C to 50°C

Storage: -20°C to 70°C

Humidity: 10% to 90% at 50°C

#### **ELECTRICAL**

- AC Input: 110-240 V, 50/60 Hz
- Power consumption: < 20W</p>
- Double Power Supply

#### **FEATURES**

Compact 1RU 19" mechanical frame

Up to 4 analog or digital stereo audio inputs (4 analog left + 4 analog right + 4 digital AES/EBU XLR connectors, in total 12 XLR connectors all on the front

Automatic Sample Rate Converter for each digital AES/EBU input (48 kHz output sample frequency with typically -117dB THD+N)

High performance analog to digital Converter for each analog input (typically - 90dB THD+N)

Secured connection with professional XLR connectors and disconnection proof USB connectors



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#### **BLOCK DIAGRAM**

